

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err ors
1	BRS	L5	814029	configuration	USPAT; EPO	2000/05/18 09:19			0
2	BRS	L6	1131768	generate	USPAT; EPO	2000/05/18 09:22		Truncation Overflow. Return string from Server is: 5'0'0'GEN	1
3	BRS	L7	147507	graphics\$	USPAT; EPO	2000/05/18 09:22			0
4	BRS	L8	210852	instance?	USPAT; EPO	2000/05/18 09:23			0
5	BRS	L9	2726	5 near 6	USPAT; EPO	2000/05/18 09:23			0
6	BRS	L10	806	instantiate	USPAT; EPO	2000/05/18 09:23			0
7	BRS	L11	86	8 same 10	USPAT; EPO	2000/05/18 09:24			0
8	BRS	L13	4	9 and 11	USPAT; EPO	2000/05/18 09:24			0
9	BRS	L14	726256	generate or generating	USPAT; EPO	2000/05/18 09:34			0
10	BRS	L16	1512	14 near 5,	USPAT; EPO	2000/05/18 09:35			0
11	BRS	L18	?	11 and 16	USPAT; EPO	2000/05/18 09:36			0
12	BRS	L19	202	8 and 16	USPAT; EPO	2000/05/18 09:37			0
13	BRS	L20	83	19 and 7	USPAT; EPO	2000/05/18 09:38			0
14	BRS	L22	6	20 and 10	USPAT; EPO	2000/05/18 09:39			0
15	BRS	L24	3	16 same 8	USPAT; EPO	2000/05/18 09:40			0

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5524212 A	19960604	37	Multiprocessor system with write generate method for updating cache	711/121	364/228.7 ; 364/241.9 ; 364/243.41 ; 364/243.5 ; 364/940.67 ; 364/964.27 ; 364/964.31 ; 364/DIG.1 ; 364/DIG.2 ; 711/141 ; 711/144
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5617514 A	19970401		Generalized configurator using multiple interacting packers and declaratively defined constraint expressions	706/45	706/46 ; 706/55
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5950011 A	19990907	15	System using designer editor and knowledge base for configuring preconfigured software in an open system in a distributed environment	717/11	713/1 ; 713/100

L24

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Inventor	S
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5475830 A	19951212	23	Structure and method for providing a reconfigurable emulation circuit without hold time violations	716/16	716/17 ; 716/7	Chen, Nang-Ping , et al.	<input type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5630025 A	19970513		Generalized configurator using a declaratively constructed two-level bi-partite graph as a knowledge representation	706/46	706/45 ; 706/55	Dolby, Nigel I. , et al.	<input type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5649167 A	19970715		Methods for controlling timing in a logic emulation system	703/23		Chen, Nang-Ping , et al.	<input type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5835751 A	19981110	26	Structure and method for providing reconfigurable emulation circuit	716/16	327/39	Chen, Nang-Ping , et al.	<input type="checkbox"/>

513

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input checked="" type="checkbox"/>		US 5287511 A	19940215	906	Architectures and methods for dividing processing tasks into tasks for a programmable real time signal processor and tasks for a decision making microprocessor interfacing therewith	717/2	717/3 ; 717/5
2	<input checked="" type="checkbox"/>		US 5524244 A	19960604	57	System for dividing processing tasks into signal processor and decision-making microprocessor interfacing therewith	717/5	717/10
3	<input type="checkbox"/>		US 5877966 A	19990302	36	System and method for creating configurations using templates	716/4	700/182 ; 700/97 ; 713/1
4	<input type="checkbox"/>		US 5890155 A	19990330	11	System and methods for providing encapsulated and performance-efficient data references in an object-oriented controller and distributed control system employing the same	707/9	707/103 ; 707/104 ; 713/200

Rd

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Inventor	S
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5515524 A	19960507	42	Method and apparatus for configuring systems	703/13		Lynch, John , et al.	<input type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5708798 A	19980113		Method and apparatus for configuring systems	703/1		Lynch, John , et al.	<input type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6002854 A	19991214	44	Method and apparatus for configuring systems	703/1	703/13 ; 703/2 ; 703/6	Lynch, John , et al.	<input type="checkbox"/>

Inventors